

PRODUCT SPECIFICATION



FLXP-1396-10D(I)

10Gb/s Duplex LC, SMF, 1310nm DFB-LD, PIN Receiver, LR 10km SFP+ Transceiver

■ FEATURES:

- ★ Support multi-protocol from 8.5Gb/s to 11.3Gb/s
- ★ Hot-Pluggable SFP+ footprint
- ★ 1310nm DFB laser transmitter
- ★ Duplex LC connector
- ★ Up to 10 km on 9/125μm SMF
- ★ Single +3.3V Power Supply
- ★ Compliant with SFF-8431 SFF-8432 and IEEE802.3ae
- ★ Industrial /Extended/ Commercial operating temperature range: -40°C to 85°C/-5°C to 85°C/0°C to 70°C Version available
- ★ RoHS compliant and Lead Free



■ APPLICATIONS:

- ★ 10GBASE-LR/LW Ethernet
- ★ 1200-SM-LL-L 10G Fiber channel
- ★ SDH/SONET Application

■ DESCRIPTION:

Flowlink FLXP-1396-10D(I) transceivers are Small Form Factor Pluggable SFP+ transceivers designed for use in 10-Gigabit multi-rate links up to 10km of G.652 single mode fiber. They are compliant with SFF-8431, SFF-8432 and 10GBASE ER/EW; support 4x, 8x and 10x Fibre Channel. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472. Flowlink FLXP-1396-10D(I) transceivers are RoHS compliant per Directive 2011/65/EU. The high performance 1310nm DFB transmitter and high sensitivity PIN receiver provide superior performance for Ethernet applications at up to 10km links.

■ Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	T_S	-40		+85	°C
Case Operating Temperature	T_A	-40		+85	°C
Supply Voltage	V_{CC}	0		3.6	V
Relative Humidity	RH	0		85	%

■ Electrical Characteristics (Condition: 25°C=T_{OP})

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	V_{CC}	3.14	3.3	3.47	V	
Supply Current	I_{CC}			350	mA	
Transmitter Section:						
Input differential impedance	R_{in}		100		Ω	1
Tx Input Single Ended DC Voltage Tolerance (Ref V _{ee} T)	V	-0.3		4	V	
Differential input voltage swing	$V_{in,pp}$	180		700	mV	2
Transmit Disable Voltage	V_D	2		V_{CC}	V	3
Transmit Enable Voltage	V_{EN}	V _{ee}		V _{ee} +0.8	V	
Receiver Section:						
Single Ended Output Voltage Tolerance	V	-0.3		4	V	
Rx Output Diff Voltage	V_o	300		850	mV	
Rx Output Rise and Fall Time	T_r/T_f	30			ps	4
LOS Fault	$V_{LOS\ fault}$	2		$V_{CC\ HOST}$	V	5
LOS Normal	$V_{LOS\ norm}$	V _{ee}		V _{ee} +0.8	V	5

Note:

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Per SFF-8431 Rev 3.0
3. Into 100 ohms differential termination.
4. 20%~80%
5. LOS is an open collector output. Should be pulled up with 4.7k – 10k Ω on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.

■ Optical Characteristics (Condition: 25°C=TOP)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter Section:						
Center Wavelength	λ_t	1290	1310	1330	nm	
spectral width	λ			1	nm	
Average Optical Power	P _{avg}	-7		0.5	dBm	1
Laser Off Power	P _{off}			-30	dBm	
Extinction Ratio	ER	3.5			dB	
Transmitter Dispersion Penalty	TDP			3.2	dB	2
Relative Intensity Noise	R _{in}			-128	dB/Hz	3
Optical Return Loss Tolerance		20			dB	
Receiver Section:						
Center Wavelength	λ_r	1290		1330	nm	
Receiver Sensitivity (2-31, 10E-12)	Sen			-15	dBm	4
Los Assert	LOS _A	-28			dBm	
Los De-assert	LOS _D			-16	dBm	
Los Hysteresis	LOS _H	0.5			dB	
Overload	Sat	0.5			dBm	5
Receiver Reflectance	R _{rx}			-12	dB	

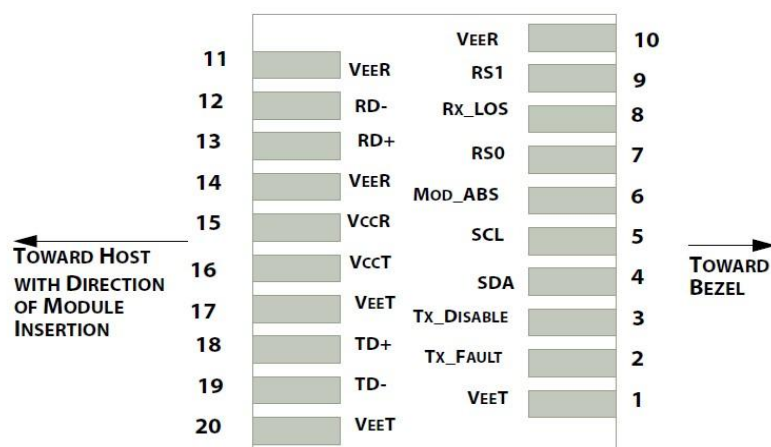
Note:

1. Average power figures are informative only, per IEEE802.3ae.
2. TWDP figure requires the host board to be SFF-8431 compliant. TWDP is calculated using the Matlab code provided in clause 68.6.6.2 of IEEE802.3ae.
3. 12dB reflection.
4. Conditions of stressed receiver tests per IEEE802.3ae. CSRS testing requires the host board to be SFF-8431 compliant.
5. Receiver overload specified in OMA and under the worst comprehensive stressed condition.

■ Timing Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit
TX_Disable Assert Time	t _{off}			10	us
TX_Disable Negate Time	t _{on}			1	ms
Time to Initialize Include Reset of TX_FAULT	t _{int}			300	ms
TX_FAULT from Fault to Assertion	t _{fault}			100	us
TX_Disable Time to Start Reset	t _{reset}	10			us
Receiver Loss of Signal Assert Time	T _{A,RX_LOS}			100	us
Receiver Loss of Signal Deassert Time	T _{d,RX_LOS}			100	us
Rate-Select Chage Time	t _{ratesel}			10	us
Serial ID Clock Time	t _{serial-clock}			100	kHz

■ Pin Assignment:



■ Pin Function Definitions

PIN #	Name	Function	Notes
1	VeeT	Module transmitter ground	1
2	Tx Fault	Module transmitter fault	2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	2
7	RS0	Rate select0, optionally control SFP+ receiver. When high, input data rate >4.5Gb/ s; when low, input data rate <=4.5Gb/s	
8	LOS	Receiver Loss of Signal Indication	4
9	RS1	Rate select0, optionally control SFP+ transmitter. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	
10	VeeR	Module receiver ground	1
11	VeeR	Module receiver ground	1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	1
18	TD+	Transmitter inverted data input	
19	TD-	Transmitter non-inverted data input	
20	VeeT	Module transmitter ground	1

Note:

1. The module ground pins shall be isolated from the module case.
2. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.
3. This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.
4. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

■ SFP Module EEPROM Information and Management

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP - 8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I²C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information (A0h) is listed in Table 2. The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h). For more details of the memory map and byte definitions, please refer to the SFF-8472, “Digital Diagnostic Monitoring Interface for Optical Transceivers”. The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

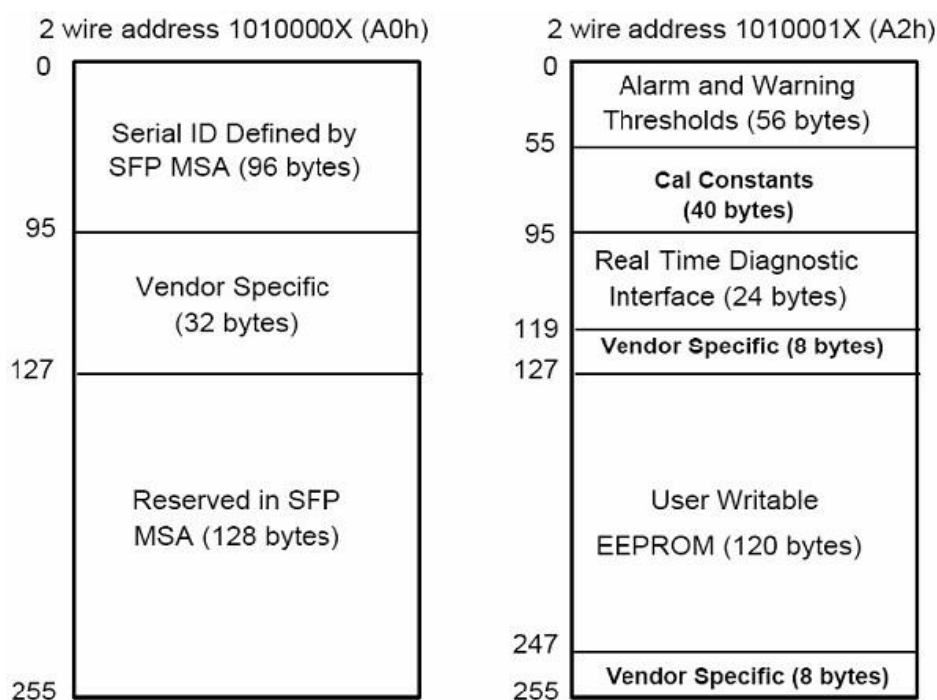


Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data	Length	Name of	Description and Contents
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Address	(Byte)	Length	
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	10G Base-LR
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name:
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "GL-XXXXXX" (ASCII)
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	
128-255	128	Reserved	Reserved for SFF-8079

■ Digital Diagnostic Monitor Characteristics

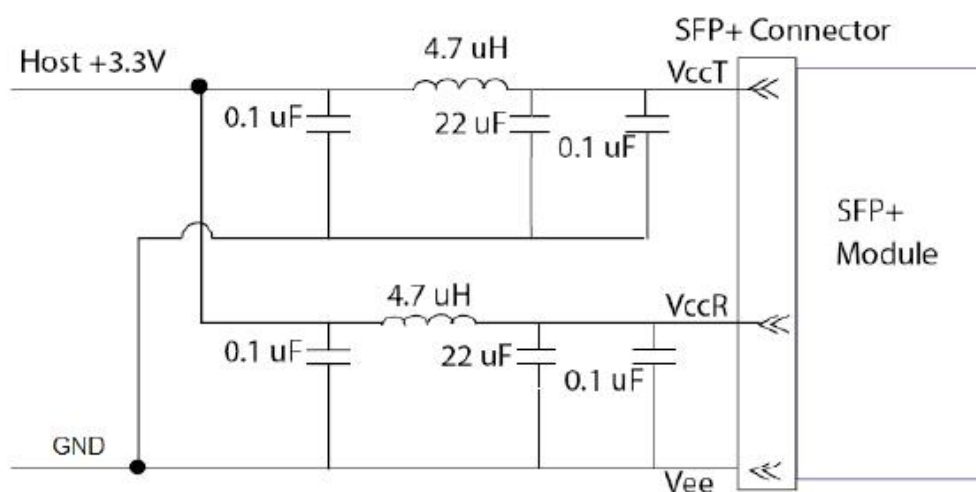
Data Address	Parameter	Accuracy	Unit	Calibraton
96-97	Transceiver Internal Temperature	±3.0	°C	internal
98-99	VCC3 Internal Supply Voltage	±5.0	%	internal
100-101	Laser Bias Current	±10	%	internal
102-103	Tx Output Power	±3.0	dBm	internal
104-105	Rx Input Power	±3.0	dBm	internal

■ Regulatory Compliance

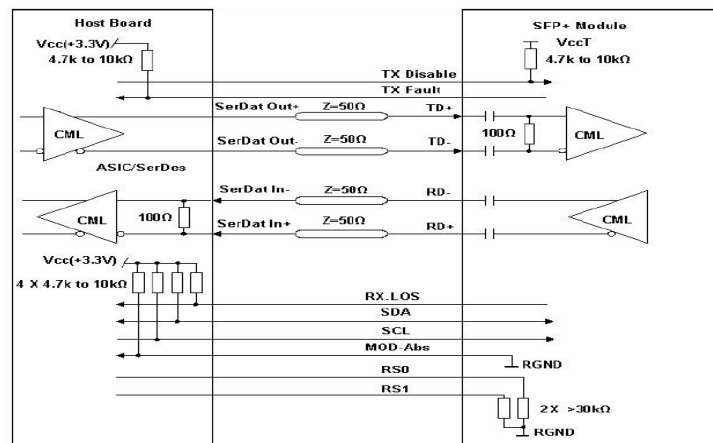
The transceiver complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000 V)
Electrostatic Discharge (ESD) to the Duplex LC Receptacle	IEC 61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class 1 laser product.

■ Recommended Circuit

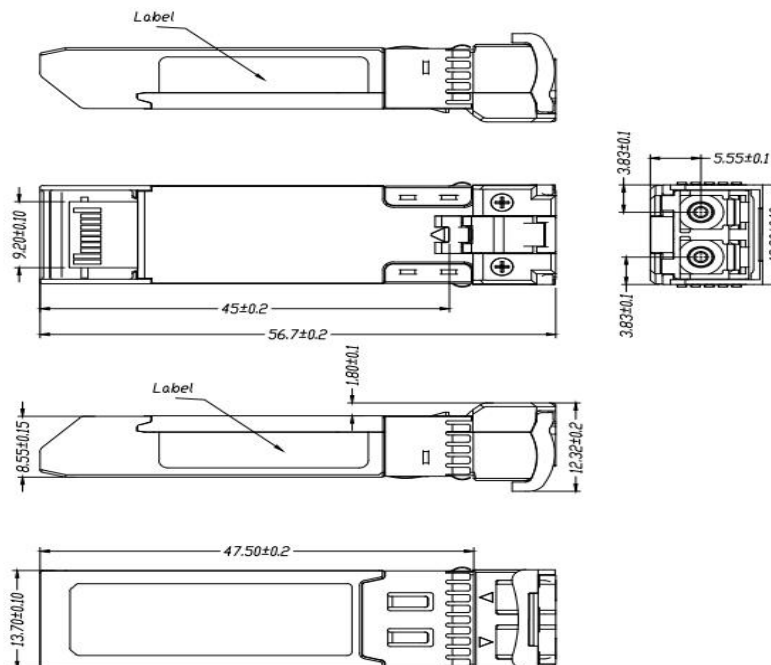


Recommended Host Board Power Supply Circuit



Recommended High-speed Interface Circuit

■ Mechanical Dimensions



Mechanical Drawing

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